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Application / Conf. No.	10/783,216 / 6678
Filing Date	February 20, 2004
First Named Inventor	Tim Tuan
Examiner Name	Nelson C. Lam
Art Unit	2825
Patent No.	
Total Number of Pages in This Submission	Attorney Docket Number <b>X-1462-1P US</b>

### ENCLOSURES (check all that apply)

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Remarks

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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X-1462-1P US  
10/783,216

PATENT  
Conf. No. 6678

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tim Tuan et al.

Assignee: Xilinx, Inc.

Title: "Tuning Programmable Logic Devices for Low-Power Design  
Implementation"

Serial No.: 10/783,216

Filing Date: 02/20/2004

Examiner: Nelson C. Lam

Art Unit: 2825

Docket No.: X-1462-1P US

Conf. No.: 6678

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

In response to the final rejection in the Second Office Action mailed from the Patent Office on September 11, 2006, Applicants submit the following amendments and remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.